

Appl. No. 10/090,916
Amendment and/or Response
Reply to Office action of 7 April 2005

Page 7 of 11

Amendments to the Claims:

A listing of the entire set of pending claims (including amendments to the claims, if any) is submitted herewith per 37 CFR 1.121. This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1. (Amended) An interface module for receiving television signals or radio signals, having several inputs ~~(7)~~ and outputs ~~(7, 9, 10, 14)~~ for receiving and distributing picture and sound signals as well as control signals and at least one input for antenna signals ~~(5a)~~, the interface module comprising at least one tuner ~~(1)~~ and a chip ~~(2)~~ with a microprocessor ~~(4)~~ including memory means and means for capturing data and with switching means ~~(41)~~, with picture, color and sound decoding means ~~(42)~~ and with signal processing means ~~(43)~~, ~~whereas~~ wherein the chip ~~(2)~~ and the tuner ~~(1)~~ are installed in a common casing ~~(3)~~.
2. (Amended) ~~An~~ The interface module as claimed in of claim 1, characterized in ~~that~~ that wherein the chip ~~(2)~~ and the tuner ~~(1)~~ are mounted on a common printed circuit board.
3. (Amended) ~~An~~ The interface module as claimed in of claim 2, characterized in ~~that~~ that wherein the common printed circuit board comprises further peripheral components and connecting interfaces partly outside the casing ~~(3)~~.
4. (Amended) ~~An~~ The interface module as claimed in of claim 1, characterized in ~~that~~ that wherein the common casing ~~(3)~~ is made of an electromagnetic shielding material.

Appl. No. 10/090,916
Amendment and/or Response
Reply to Office action of 7 April 2005

Page 8 of 11

5. (Amended) ~~An~~ The interface module as claimed in ~~of~~ claim 1, characterized in that wherein the microprocessor-(4) in the chip-(2) and the tuner-(1) are connected by a digital signal bus.

6. (Amended) ~~An~~ The interface module as claimed in ~~of~~ claim 3, characterized in that wherein the microprocessor-(4) in the chip-(2) is designed to control the tuner-(1) via the digital signal bus and to perform tasks and functions of the tuner-(1) by software means stored in a one-time programmable memory of the microprocessor (4).

7. (Amended) ~~An~~ The interface module as claimed in ~~of~~ claim 6, characterized in that wherein the interface module is designed to be initialized by software stored in the one-time programmable memory of the microprocessor-(4).

8. (Amended) ~~An~~ The interface module as claimed in ~~of~~ claim 6, characterized in that wherein the interface module is designed to store software for a customized user interface in the one-time programmable memory of the microprocessor-(4).

9. (Amended) ~~An~~ The interface module as claimed in ~~of~~ claim 6, characterized in that wherein correction parameters obtained by tuner and module alignment and measurements can be stored in the memory of the microprocessor-(4) and that correction control circuits are provided.

10. (Amended) ~~An~~ The interface module as claimed in ~~of~~ claim 6, characterized in that wherein an automatic tracking filter alignment is provided.

11. (Amended) ~~An~~ The interface module as claimed in ~~of~~ claim 6, characterized in that wherein means for variable band switching are provided and that means for measuring the strength of the received antenna signals ~~(5a, 5b)~~ are provided.

Appl. No. 10/090,916
Amendment and/or Response
Reply to Office action of 7 April 2005

Page 9 of 11

12. (Amended) ~~An~~ The interface module ~~as claimed in~~ of claim 1, ~~characterized in that wherein~~ there are provided at least two antenna inputs ~~(5a, 5b)~~.

13. (Amended) ~~An~~ The interface module ~~as claimed in~~ of claim 12, ~~characterized in that wherein~~ there is provided an additional FM radio RF input ~~(5b)~~ possibility and that the interface module is designed to be able to receive TV and FM radio signals.

14. (Canceled)

15. (New) The interface module of claim 6, wherein there are provided at least two antenna inputs.